

Fig. 1 (Prior Art)

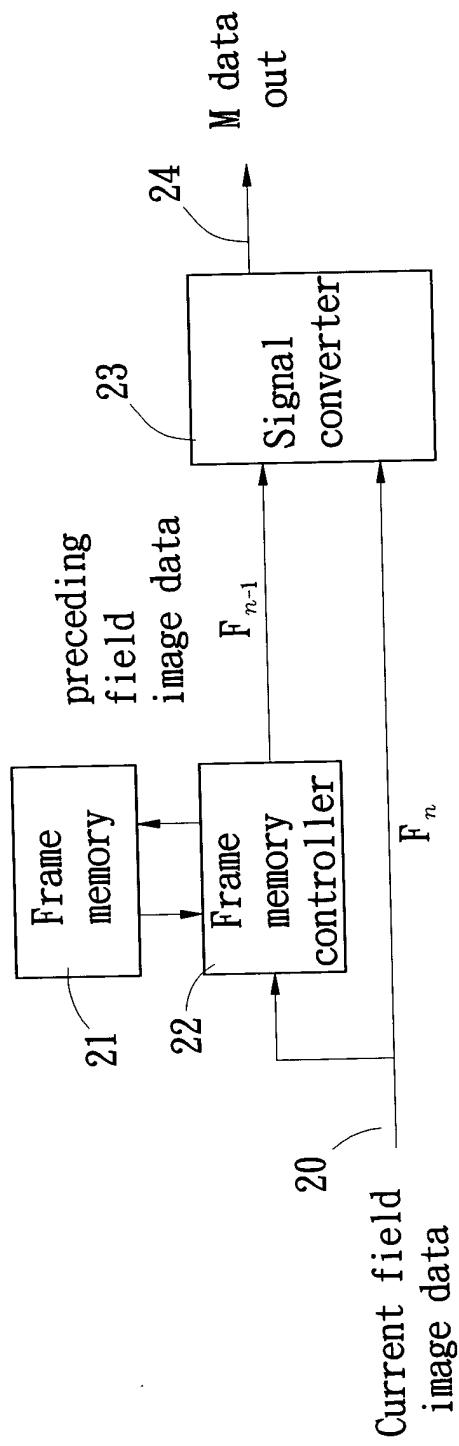


Fig. 2 (Prior Art)

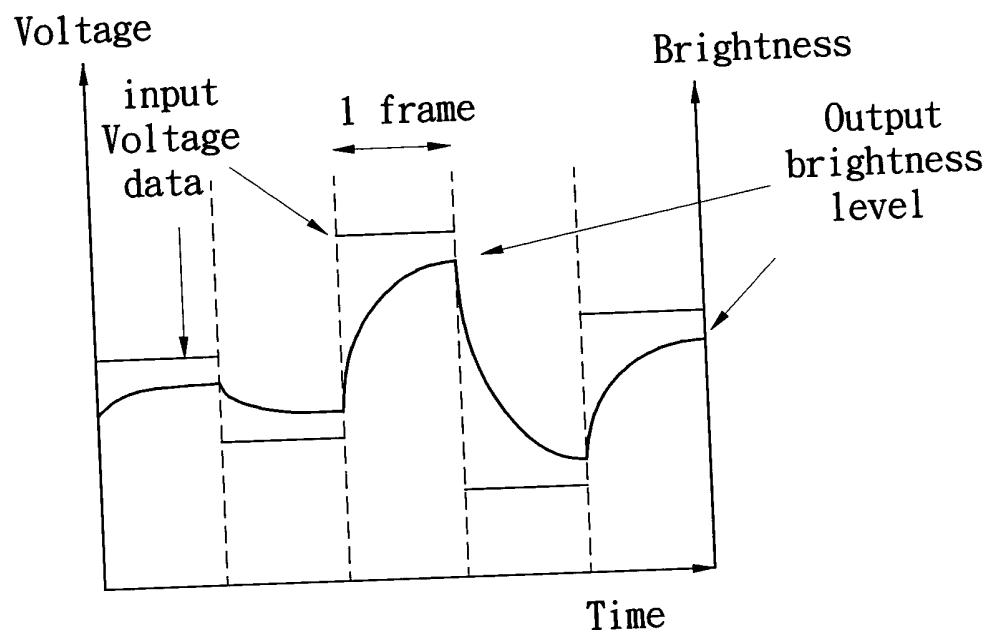


Fig. 3A (Prior Art)

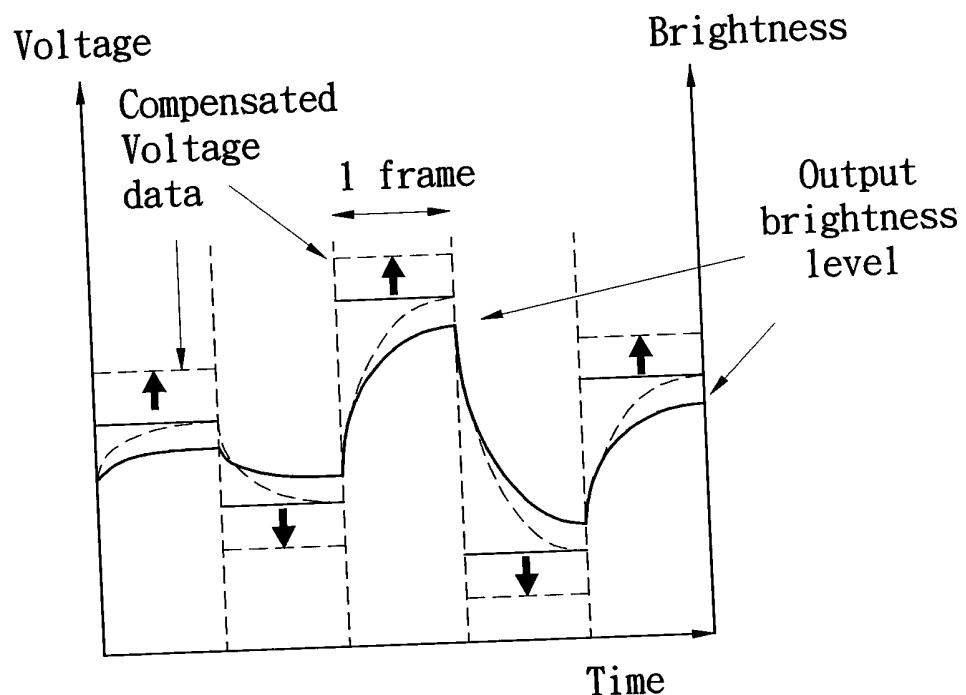


Fig. 3B (Prior Art)

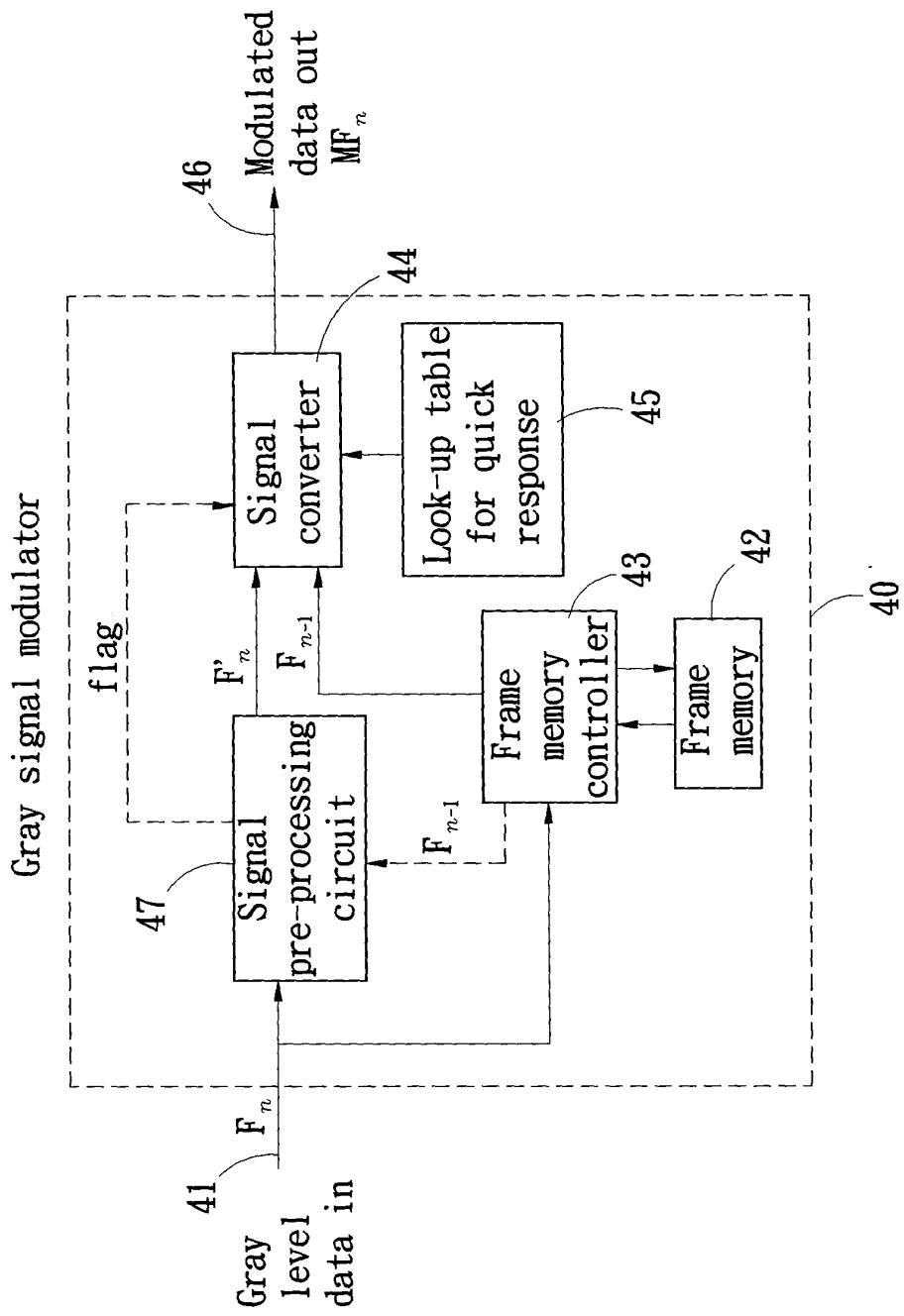


Fig. 4

Pre-processing Circuit
-for Noise Reduction

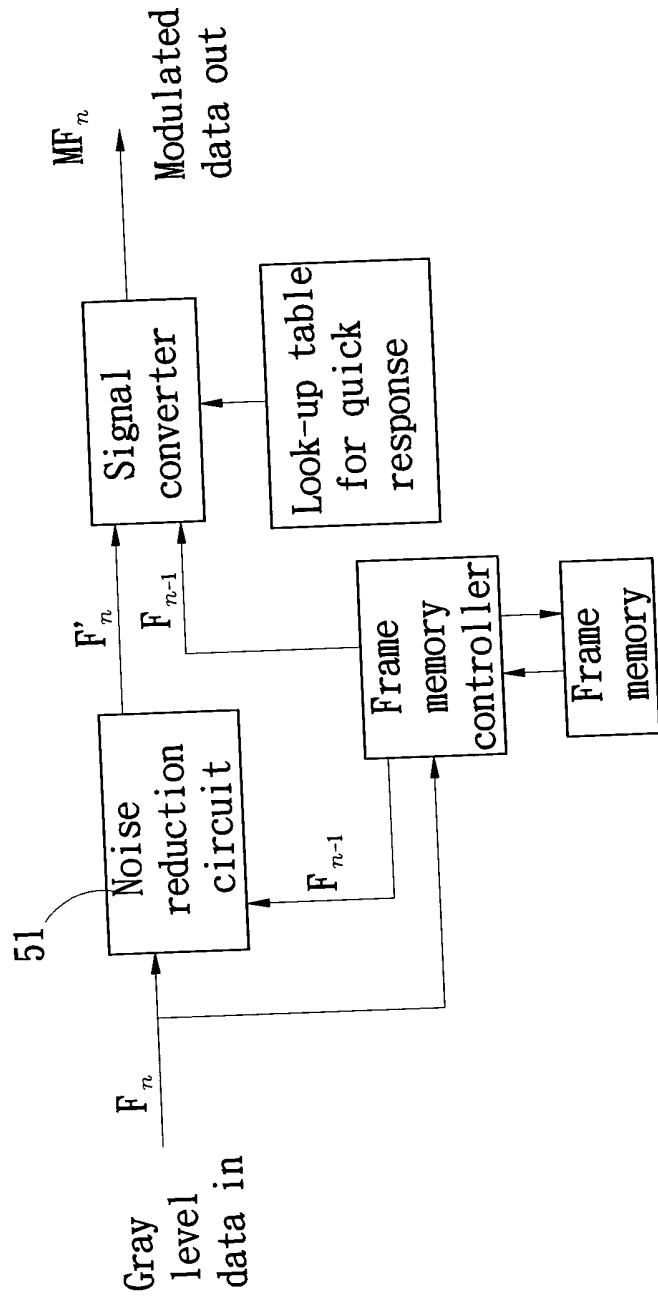


Fig. 5

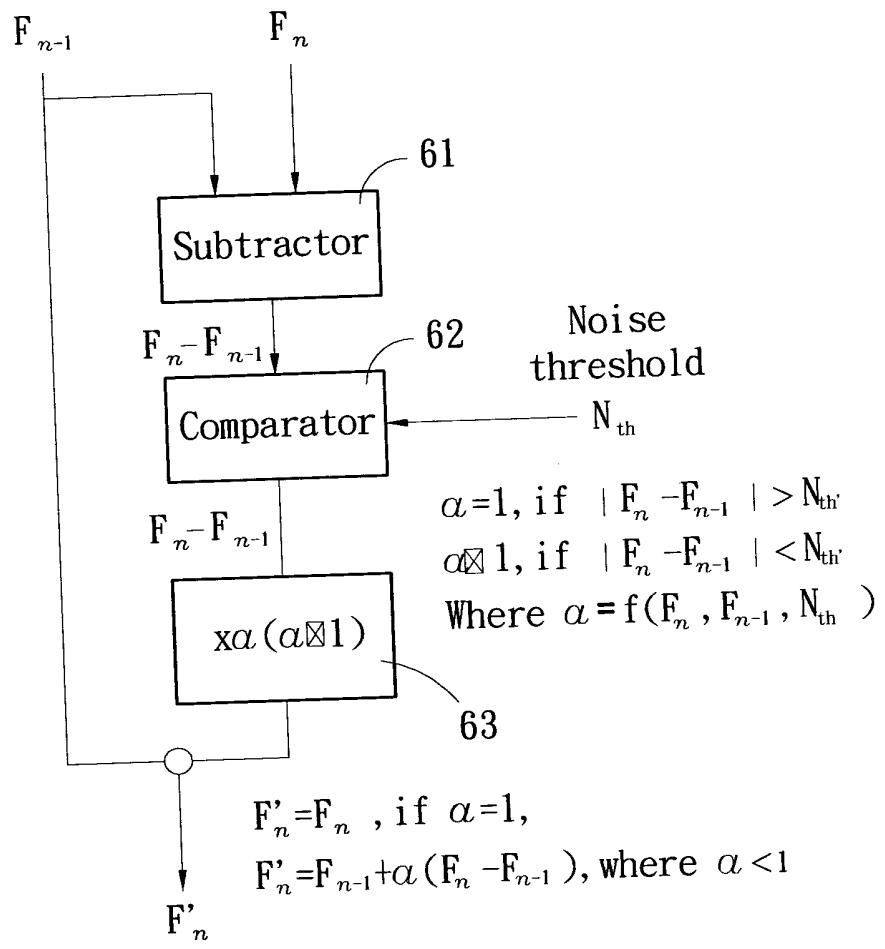


Fig. 6A

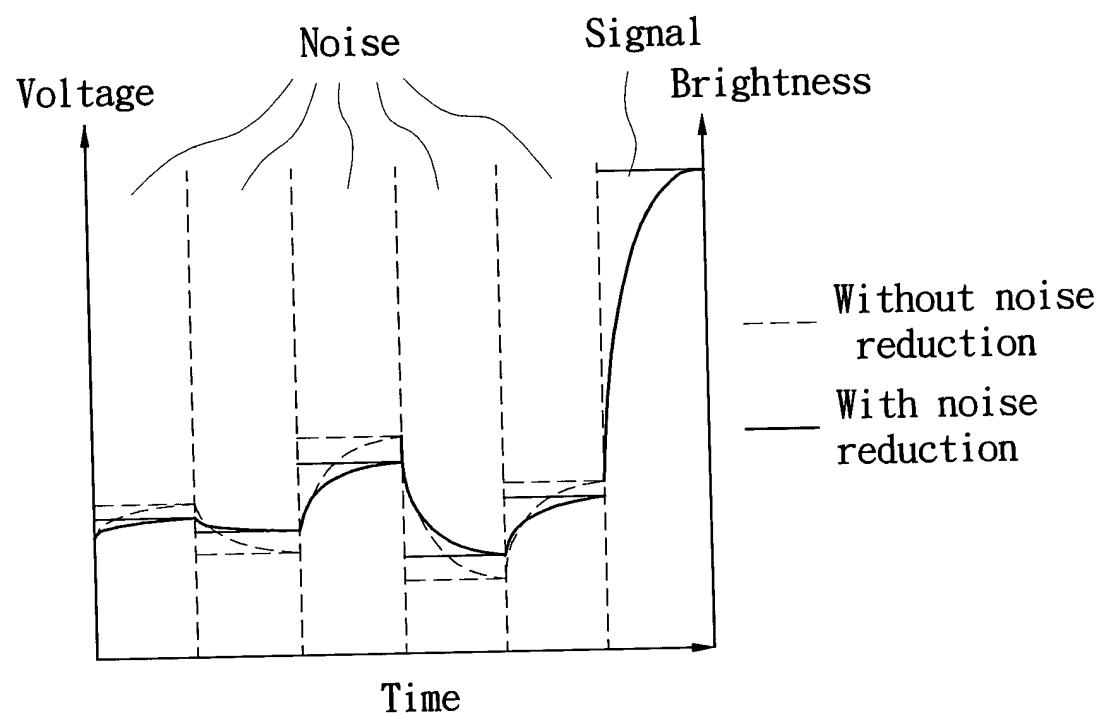


Fig. 6B

Pre-processing Circuit
-for Frame Rate Detection

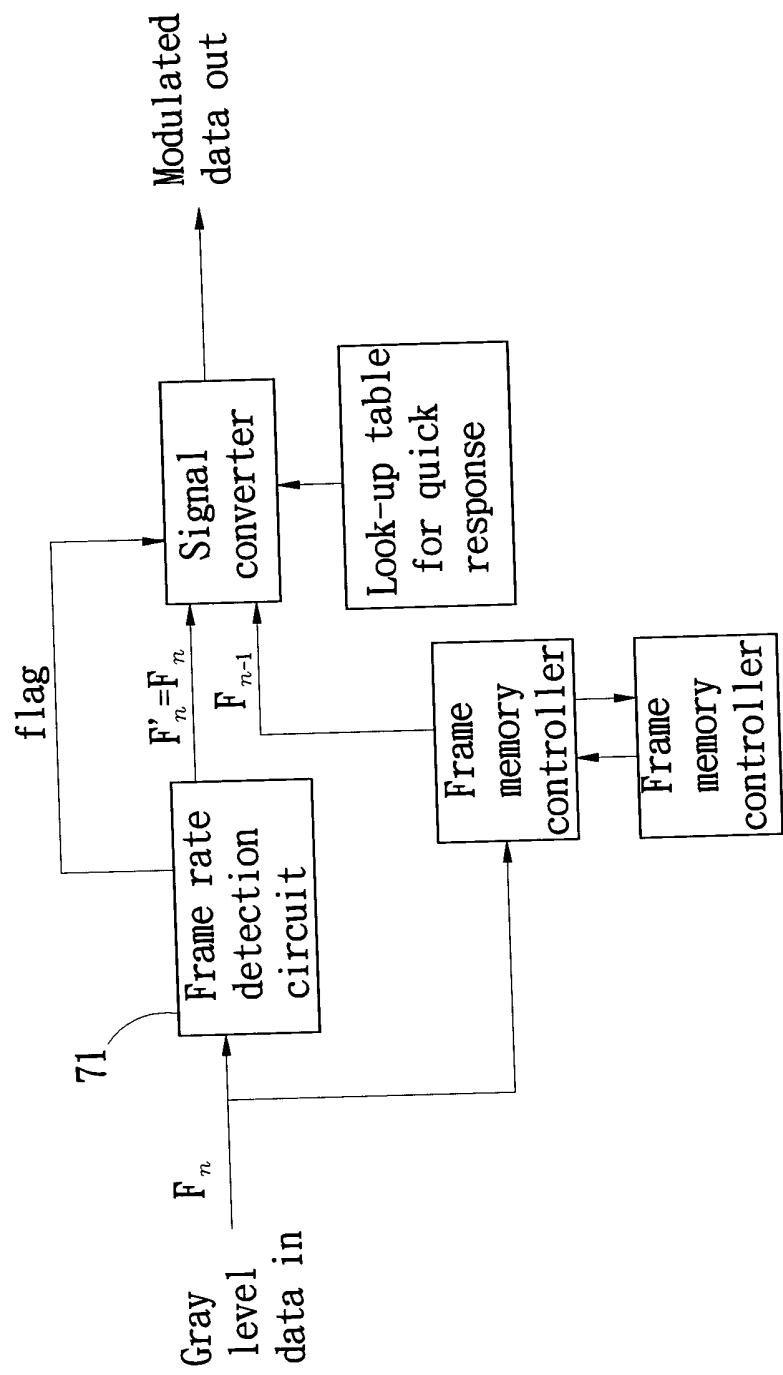


Fig. 7

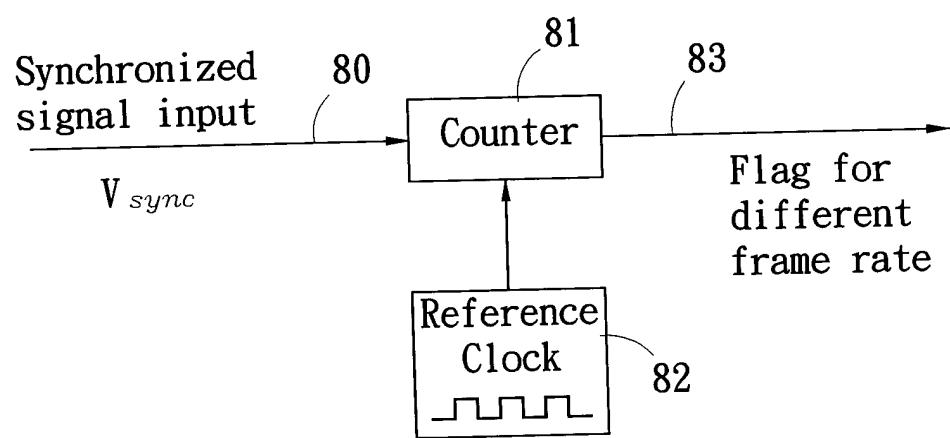


Fig. 8